

Fig. 1a

Fig. 1b

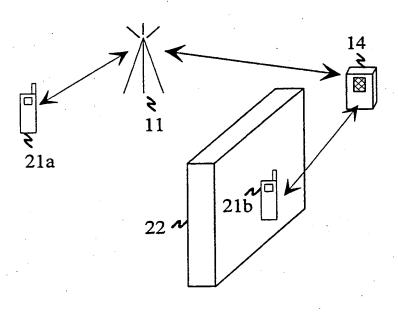
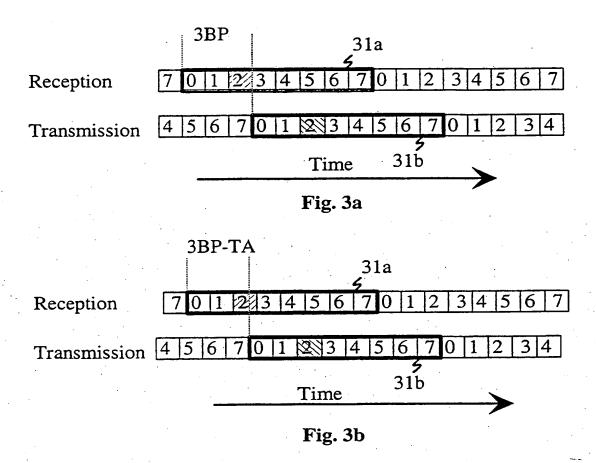
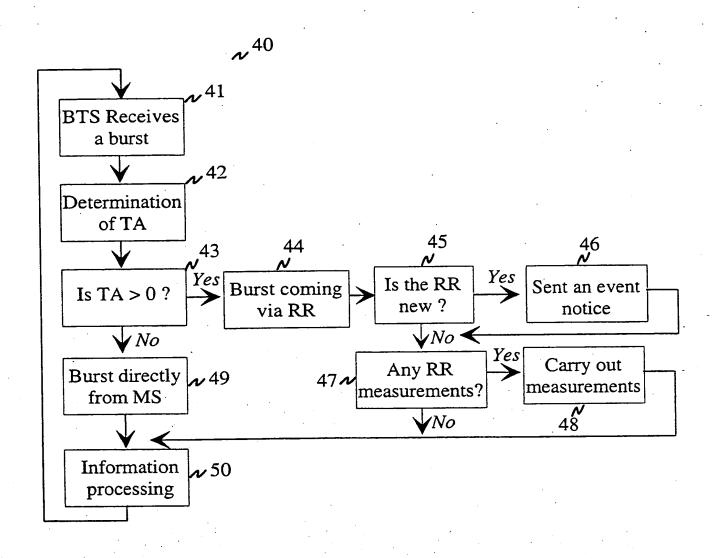


Fig. 2

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Fig. 4

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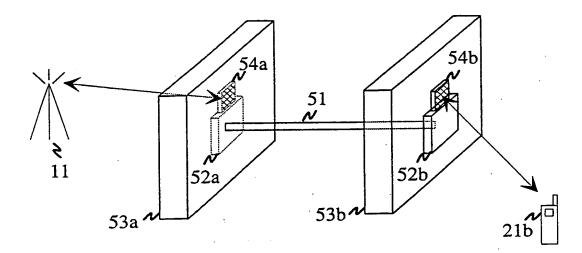


Fig. 5

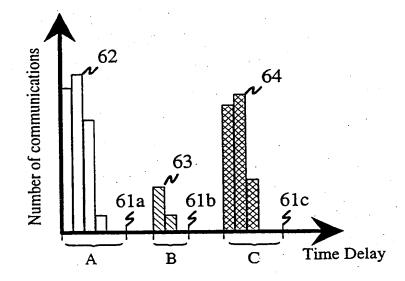


Fig. 6

5/5 70 **X** Memory 73 Program ام 72 21a 21b **Processor** ر مح 75 N I/O BTS NMS BSC 72 Fig. 7a 70 Memory 73 21a Program 71 21b≥□ 72 Processor ₽⁷⁶ 75 I/O BSC BTS NMS

Fig. 7b

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